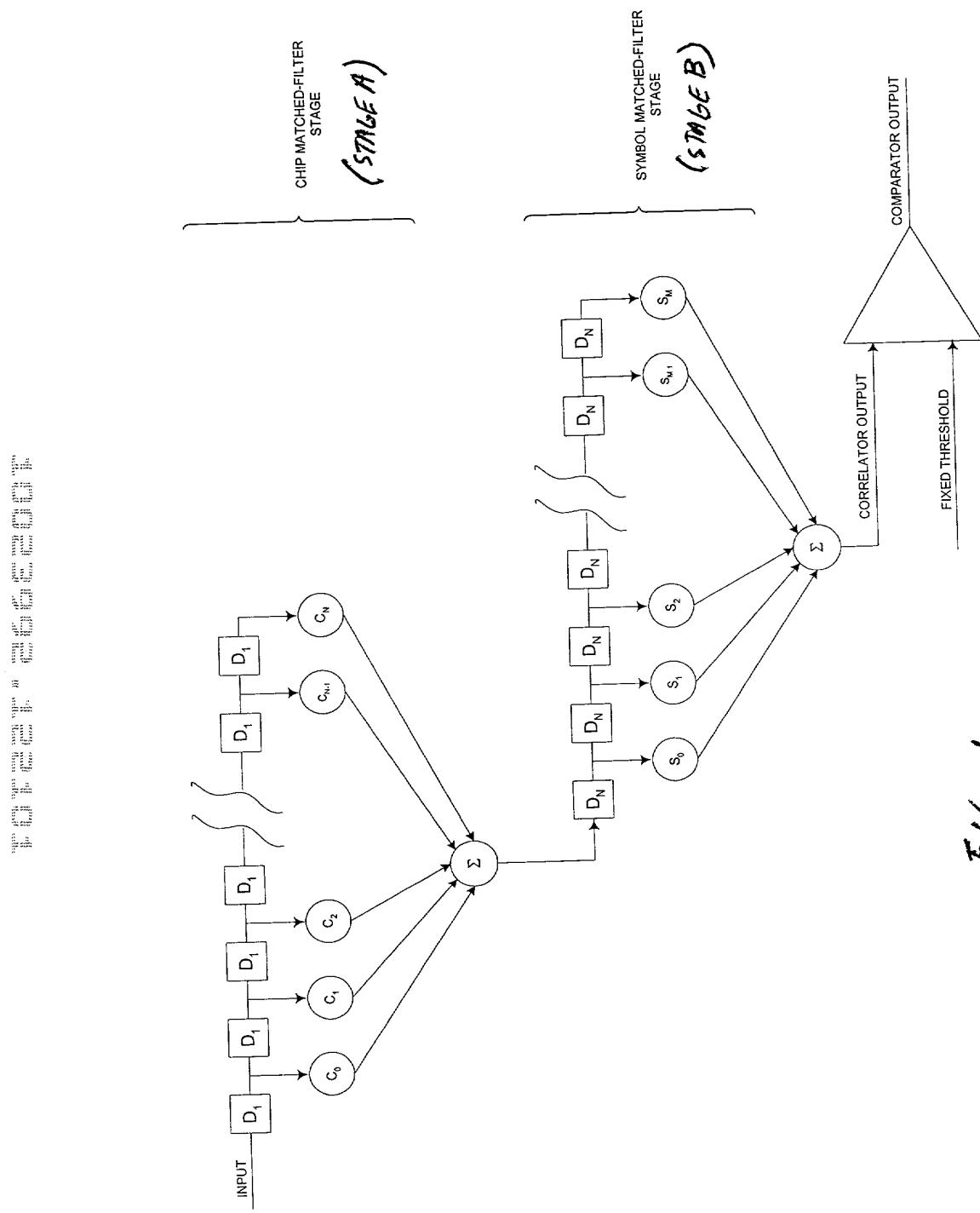
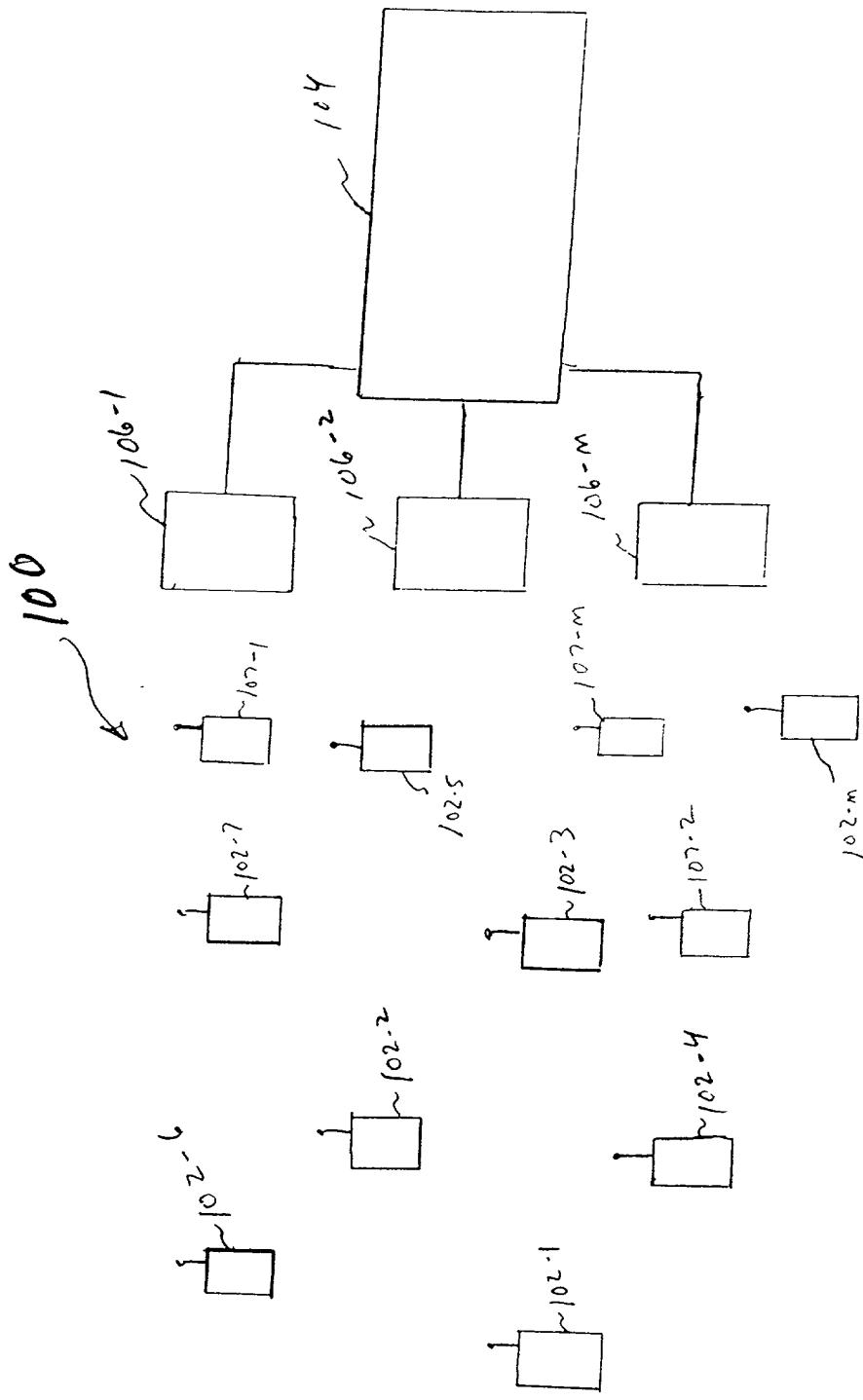


Fig. 1



F16. 2



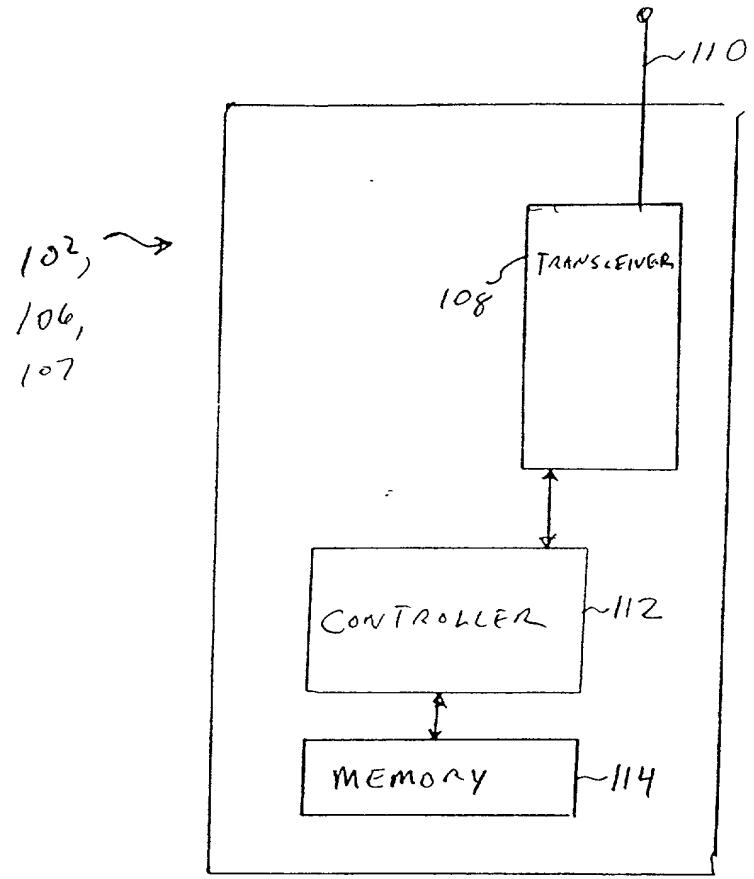


FIG. 3

INPUT
CHIP MATCHED-FILTER STAGE
SYMBOL MATCHED-FILTER STAGE
CORRELATOR OUTPUT

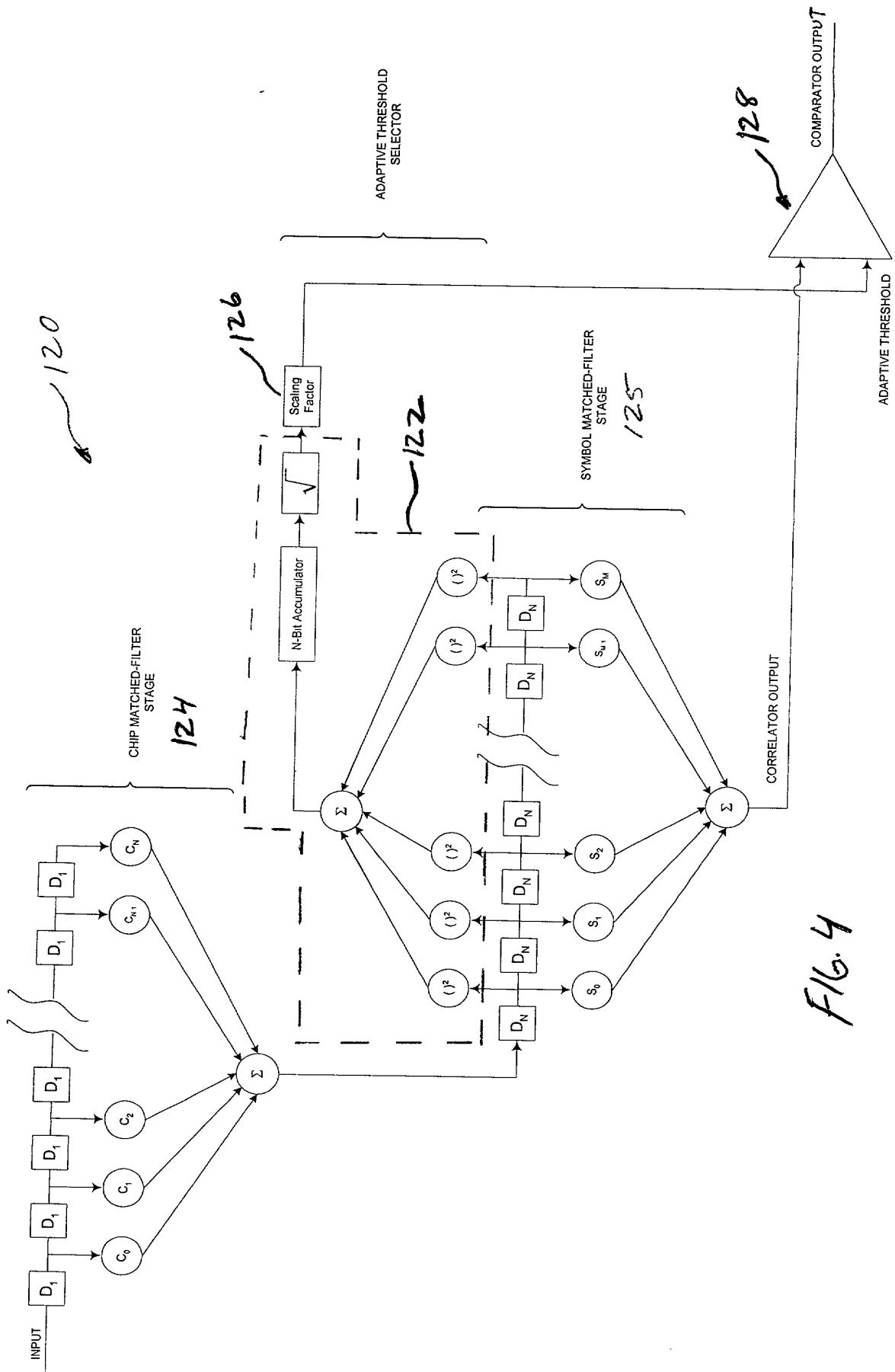


Fig. 4

TRANSMITTED WAVEFORM
STRUCTURE

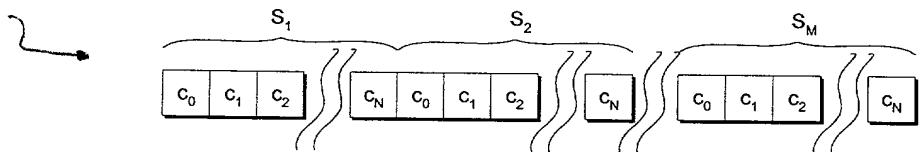


FIG. 5